

Page 1 of 3

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT					ATTY. DOCKET NO. 1875.138000F	APPLICATION NO. (to be assigned)		
					APPLICANTS Ralph Duncan et al.			
					FILING DATE Herewith	GROUP (to be assigned)		
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE	
<i>DR</i>	AA1	4,211,975	07/1980	Kuroda				
<i>DR</i>	AB1	4,789,976	12/1988	Fujishima				
<i>DR</i>	AC1	4,818,903	04/1989	Kawano				
<i>DR</i>	AD1	4,903,329	02/1990	Marik et al.				
<i>DR</i>	AE1	5,031,233	07/1991	Ragan				
<i>DR</i>	AF1	5,200,826	04/1993	Seong				
<i>DR</i>	AG1	5,311,318	05/1994	Dobrovolny				
<i>DR</i>	AH1	5,329,143	07/1994	Chan et al.				
<i>DR</i>	AI1	5,389,898	02/1995	Taketoshi et al.				
<i>DR</i>	AJ1	5,423,076	06/1995	Westergren et al.				
<i>DR</i>	AK1	5,428,829	06/1995	Osburn et al.				
FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
<i>DR</i>	AL1	37 23 778 A1	01/1988	DE	H03L	7/10	X Yes No	
<i>DR</i>	AM1	195 06 324 C1	10/1995	DE	H03H	21/00	X Yes No	
<i>DR</i>	AN1	0 393 717 A2 & A3	10/1990	EP	H03H	11/12	Yes No	
<i>DR</i>	AO1	0 431 887 A2 & A3	06/1991	EP	H03L	1/02	Yes No	
<i>DR</i>	AP1	0 535 536 A1	04/1993	EP	H01L	27/02	Yes No	
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)								
<i>DR</i>	AR	1	Burghartz, J. N. et al., "High-Q Inductors in Standard Silicon Interconnect Technology and its Application to an Integrated RF Power Amplifier," <i>International Electron Devices Meeting Technical Digest</i> , IEEE, pp. 1015-1017 (December 10-13, 1995).					
<i>DR</i>	AS	1	Craninckx, J. and Steyaert, M.S., "A 1.8-Ghz Low-Phase-Noise CMOS VCO Using Optimized Hollow Spiral Inductors," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 5, IEEE, pp. 736-744 (May, 1997).					
<i>DR</i>	AT	1	Long, J.R. and Copeland, M.A., "The Modeling, Characterization, and Design of Monolithic Inductors for Silicon RF IC's," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 5, IEEE, pp. 357-369 (March, 1997).					
EXAMINER <i>MWS</i>					DATE CONSIDERED <i>5/16/95</i>			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.								

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 <u>INFORMATION DISCLOSURE STATEMENT</u>		ATTY. DOCKET NO. 1875.138000F	APPLICATION NO. (to be assigned)
		APPLICANTS Ralph Duncan et al.	
		FILING DATE Herewith	GROUP (to be assigned)

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
<i>DR</i>	AA2	5,446,311	08/1995	Ewen et al.			
<i>DR</i>	AB2	5,483,207	01/1996	Gabara			
<i>DR</i>	AC2	5,625,325	04/1997	Rotzoll et al.			
<i>DR</i>	AD2	5,630,214	05/1997	Yamamoto et al.			
<i>DR</i>	AE2	5,648,744	07/1997	Prakash et al.			
<i>DR</i>	AF2	5,686,864	11/1997	Martin et al.			
<i>DR</i>	AG2	5,715,012	02/1998	Patel et al.			
<i>DR</i>	AH2	5,717,243	02/1998	Lowther			
<i>DR</i>	AI2	5,737,035	04/1998	Rotzoll			
<i>DR</i>	AJ2	5,739,587	04/1998	Sato			
<i>DR</i>	AK2	5,825,600	10/1998	Watt			

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
<i>DR</i>	AL2	0 629 040 A1	12/1994	EP	H03D	1/22	Yes No
<i>DR</i>	AM2	0 780 853 A1	06/1997	EP	H01F	17/00	Yes No
<i>DR</i>	AN2	2 058 505 A	04/1981	GB	H03D	7/12	Yes No
<i>DR</i>	AO2	62-154607	07/1987	JP	H01F	15/04	X Yes No
<i>DR</i>	AP2	WO 97/09786 A1	03/1997	PCT	H03L	7/08	Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>DR</i>	AR	<u>2</u>	Poole, S.J. et al., "A CMOS Subscriber Line Audio Processing Circuit Including Adaptive Balance," <i>IEEE International Symposium on Circuits and Systems Proceedings</i> , Vol. 2 of 3, IEEE, pp. 1931-1934 (1988).
<i>DR</i>	AS	<u>2</u>	Yue, C.P. and Wong, S.S., "On-Chip Spiral Inductors with Patterned Ground Shields for Si-Based RF IC's," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 33, No. 5, IEEE, pp. 743-752 (May, 1998).
<i>DR</i>	AT	<u>2</u>	English-language Abstract of Japanese Patent Publication No. 62-154607, 2 pages (July 9, 1987) - Date of Publication of Application).

EXAMINER

*MWS*

DATE CONSIDERED

*5/16/95*

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 <u>INFORMATION DISCLOSURE STATEMENT</u>		ATTY. DOCKET NO. 1875.138000F	APPLICATION NO. (to be assigned)
		APPLICANTS Ralph Duncan et al.	
		FILING DATE Herewith	GROUP (to be assigned)

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA3	5,828,589	10/1998	Degenhardt			
	AB3	6,150,891	11/2000	Welland et al.			
	AC3	6,177,964 B1	01/2001	Birleson et al.			
	AD3	6,426,680 B1	07/2002	Duncan et al.			
	AE3						
	AF3						
	AG3						
	AH3						
	AI3						
	AJ3						
	AK3						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL3	WO 98/50956 A1	11/1998	PCT	H01L	29/00	Yes No
	AM3						Yes No
	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

OTHER (including Author, Title, Date, Pertinent Pages, etc.)							
	AR	3					
	AS	3					
	AT	3					

EXAMINER	<i>MWS</i>	DATE CONSIDERED	<i>5/16/15</i>
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.			